

ONS00461
U.S.P. 7144538
S/N: 10/603257

10/603257

COFL

ONS00461

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Date:

IN RE PATENT OF

PATENT NO.:

ISSUED:

FOR:



7/30/2007

Yeu Wen Lee ET AL

7144538

12/5/2006

METHOD FOR MAKING A DIRECT
CHIP ATTACH DEVICE AND
STRUCTURE

Group: 1732

Certificate of Correction

BOX:

REQUEST UNDER 35 U.S.C. 254

REQUEST FOR CERTIFICATE OF CORRECTION OF APPLICANTS MISTAKE

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Certificate

AUG 09 2007

of Correction

S I R :

In accordance with 37 CFR 1.323, Applicants hereby request the Commissioner of Patents and Trademarks to make correction of the patent identified above.

Applicants first amended claim 2 on January 30, 2006, by adding the word reversibly in the last line.

2. (currently amended): The method of claim 1 wherein the step of placing the electronic chip and the lead frame structure into the molding apparatus includes placing the electronic chip and the lead frame structure into the molding apparatus, wherein the well portion has a plurality of pins reversibly coupled to the first surface.

In Applicants amendment of claim 2 on July 17, 2006, the word 'removable' was added and 'reversibly coupled' was deleted. Applicant's mistakenly deleted the word 'coupled'. The claim was not further amended and the error went undetected.

2. (currently amended): The method of claim 1 wherein the step of placing the electronic chip and the lead frame structure into the molding apparatus includes placing the electronic chip and the lead frame structure into the molding apparatus, wherein

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the well portion has a plurality of removable pins ~~reversibly~~
~~coupled~~ to the first surface.

The reading of the claim is not clear without the word
"coupled".

This error was due to the fault of the applicants and
therefore the Commissioner is authorized to charge the fee set
forth in §1.20(a) in the amount of \$100 to deposit account 50-
1086. Certificate of Correction form TO/SB/44 is enclosed in
triplicate.

Please address any inquiries with respect to this matter
to Kevin B. Jackson at 602 244 4885.

Respectfully submitted,

YLEE, YEU WEN et al.



ON Semiconductor
Intellectual Property Dept.
P.O. Box 62890 - A700
Phoenix, AZ 85082-2890

Kevin B. Jackson
Reg. No. 38,502
Phone: 480-244-4885
Fax: 480-244-3169

Customer #64296

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 7144538

APPLICATION NO.: 10/603257

ISSUE DATE : 12/5/2006

INVENTOR(S) :
Yeu Wen Lee et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim 2: column 5, line 3, insert the word "coupled" between "pins" and "to".

MAILING ADDRESS OF SENDER (Please do not use customer number below):

SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC
Intellectual Property Department - A700
5005 E. McDowell Road
Phoenix, Arizona 85008

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer.

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